

STM32MP25XXAJ_LPDDR4

Schematics not complete, only for layout example

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U_STM32MP25XXAJ_LPDDR4_TOP
STM32MP25XXAJ_LPDDR4_TOP.SchDoc



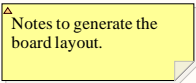
Note

Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.



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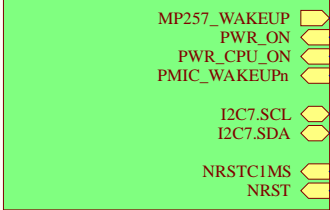
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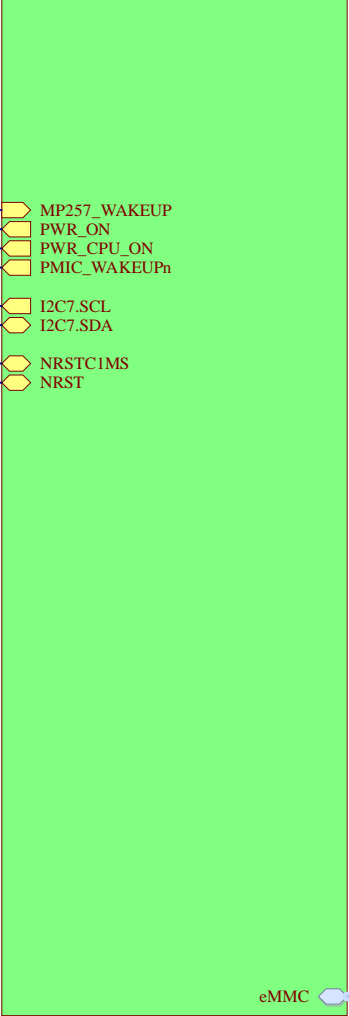


U_STM32MP25XXAJ Power
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U_PMIC
PMIC.SchDoc



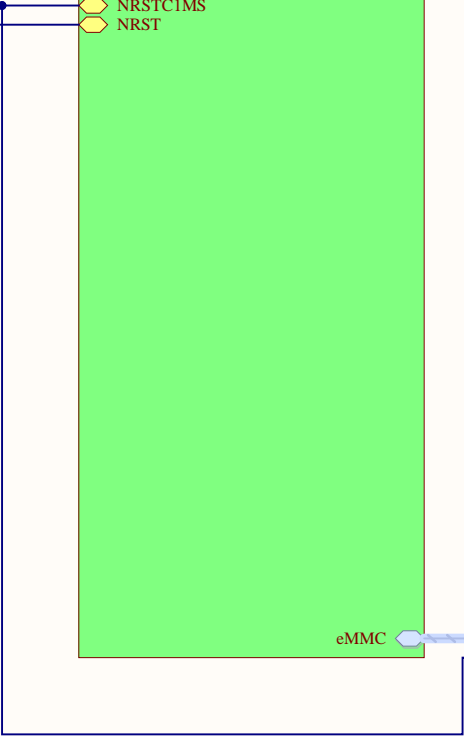
U_STM32MP25XXAJ I_Os
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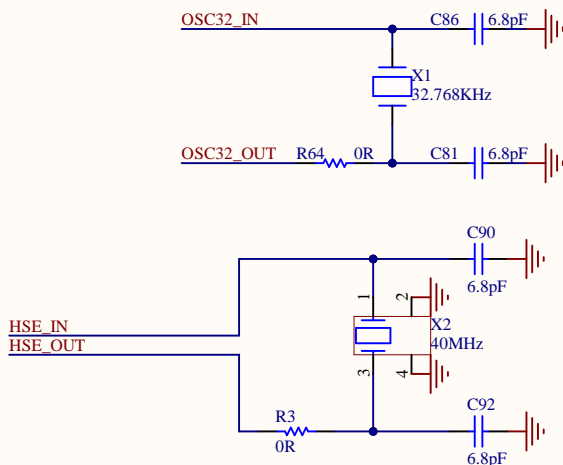
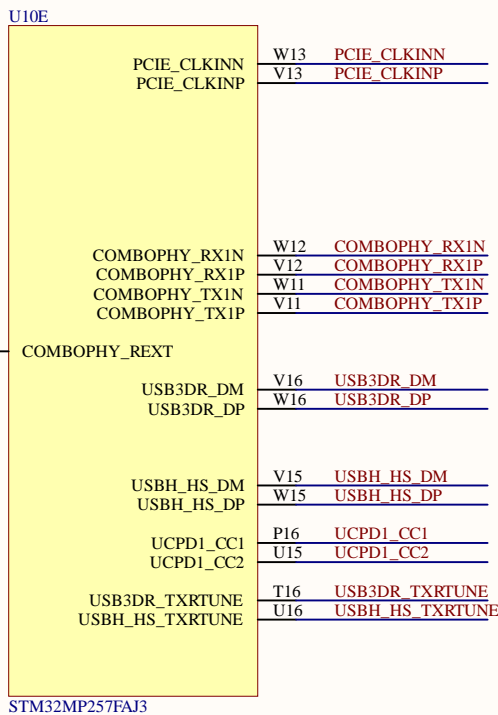
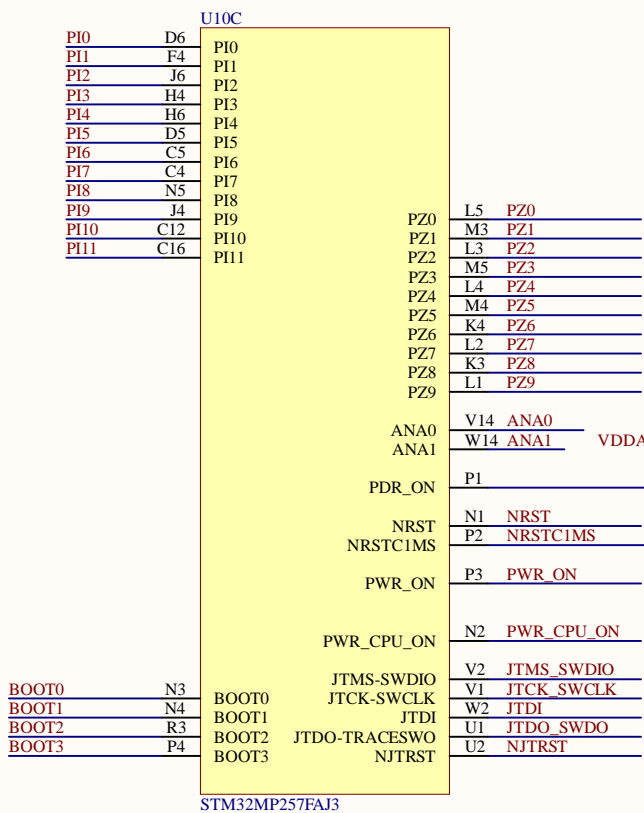
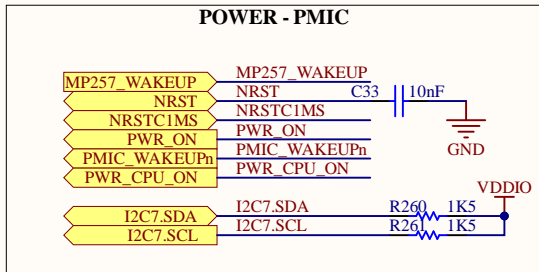


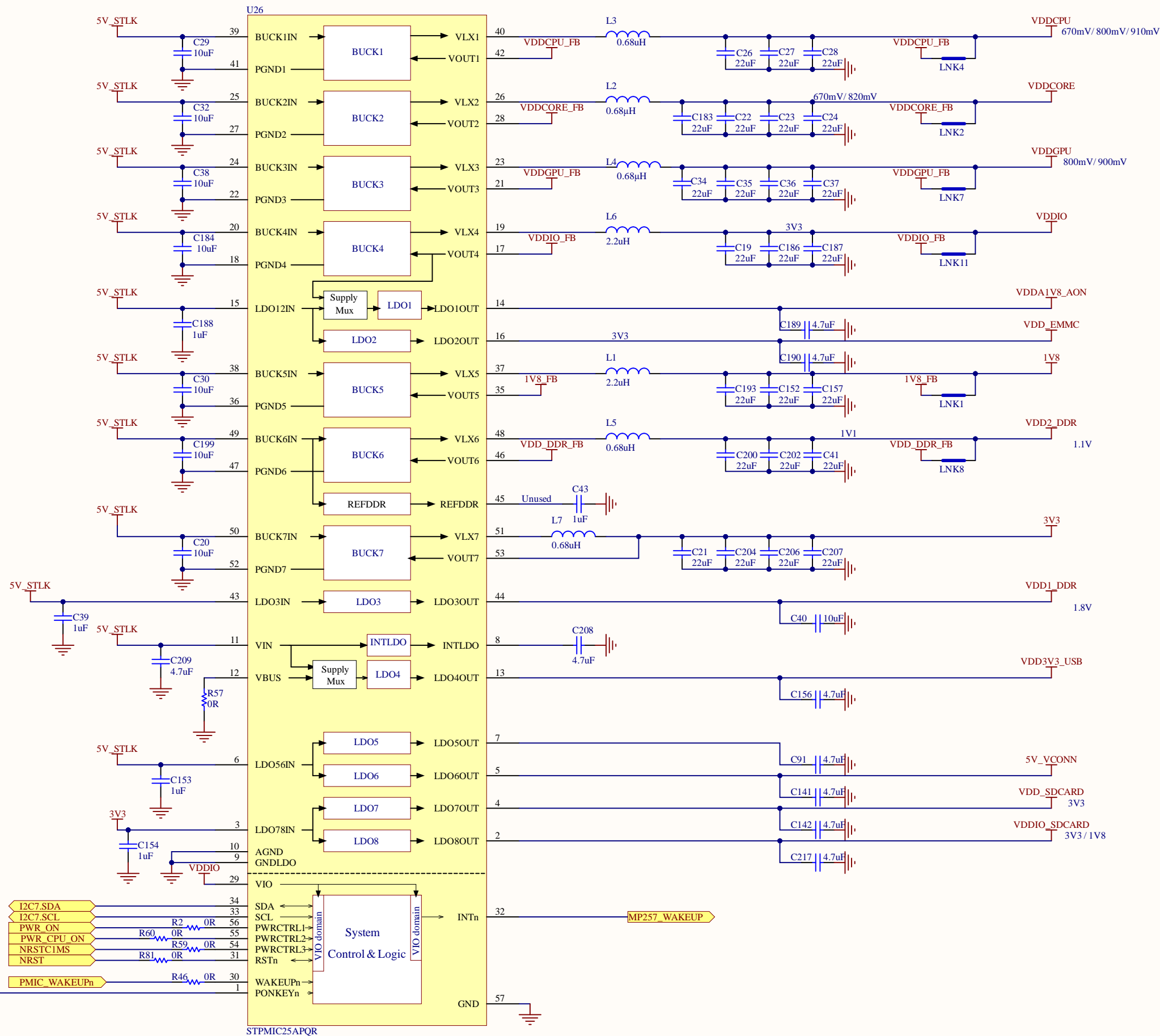
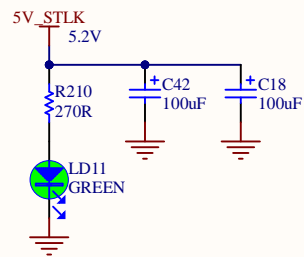
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U_eMMC
eMMC.SchDoc







Power MPU

